



Thursday, the 21st of October, 2010

ANSOFT University Day

Classroom PA-649, 6th floor, DREL

Preliminary program:

- 9:00 – 9:05 Welcome
- 9:05 – 9:20 Overview ANSYS Inc.
- 9:20 – 10:00 Introduction HFSS
- 10:00 – 10:45 Applying HFSS Solver Technology
- 10:45 – 11:00 Coffee Break
- 11:00 – 11:15 ANSYS Licensing Model
- 11:15 – 12:00 Modeling and User Interface Features in HFSS
- 12:00 – 13:00 Lunch
- 13:00 – 13:30 Ports and Sources in HFSS
- 13:30 – 14:00 Circuit / EM cosimulation
- 14:00 – 14:15 HFSS Antenna Wizard
- 14:15 – 14:30 Coffee Break
- 14:30 – 14:50 ANSYS – Ansoft Integration
- 14:50 – 15:30 PI/SI and EMC for Printed Circuit Boards (PCB)
- 15:30 – 16:00 User's Applications:**
Short presentations of ANSYS models can be submitted
in PDF or PowerPoint to raida@feec.vutbr.cz
by October 18, 2010
- 16:00 – 16:30 Open Q&A with an ANSYS Application Expert

